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APPLICANTS

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** CONTINUING DATA ***************************

This appln claims benefit of 60/454,102 03/11/2003
and is a CIP of 10/107,052 03/28/2002
which is a DIV of 09/662,735 09/15/2000 PAT 6,408,500
This application 10/695,245
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which is a 371 of PCT/US01/28669 09/14/2001
This application 10/695,245
is a CIP of 10/439,595 05/16/2003
which claims benefit of 60/397,167 07/18/2002
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(Verified)
J.m.H.

** FOREIGN APPLICATIONS ***********************

N/A   J.m.H.

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **
** 08/11/2004

Foreign Priority claimed ☐ yes ☑ no 35 USC 119 (a-d) conditions met ☐ yes ☐ no ☐ Met after Allowance Verified and Acknowledged _____ Examiner's Signature ____ Initials	STATE OR COUNTRY OR	SHEETS DRAWING 6	TOTAL CLAIMS 43	INDEPENDENT CLAIMS 6

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TITLE
Apparatus and method for use in testing a semiconductor wafer

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